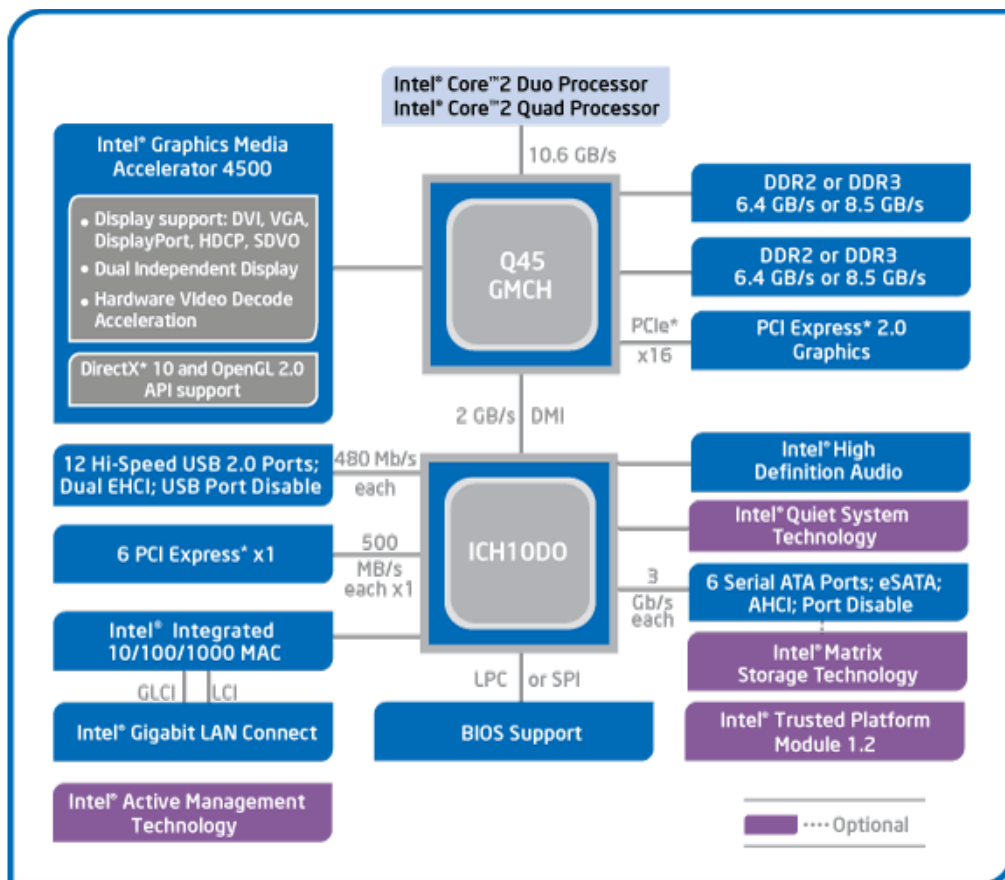
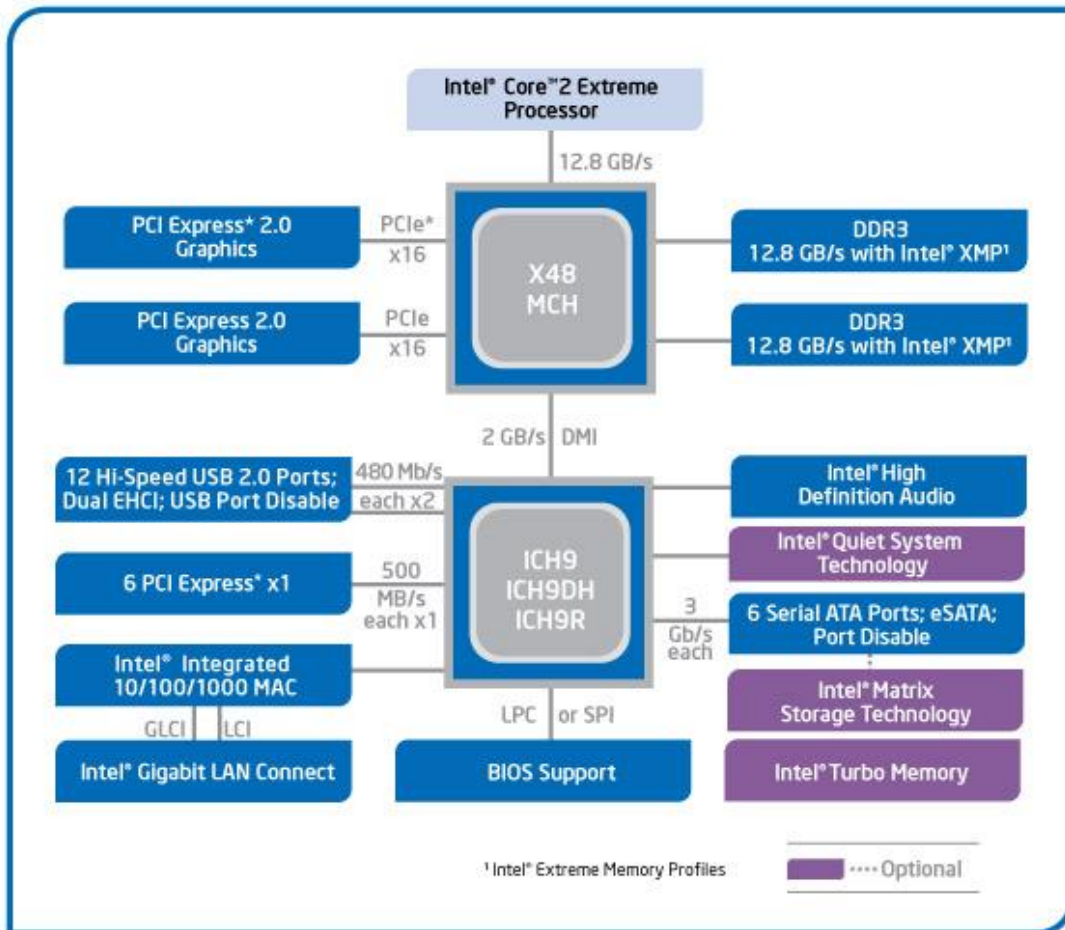


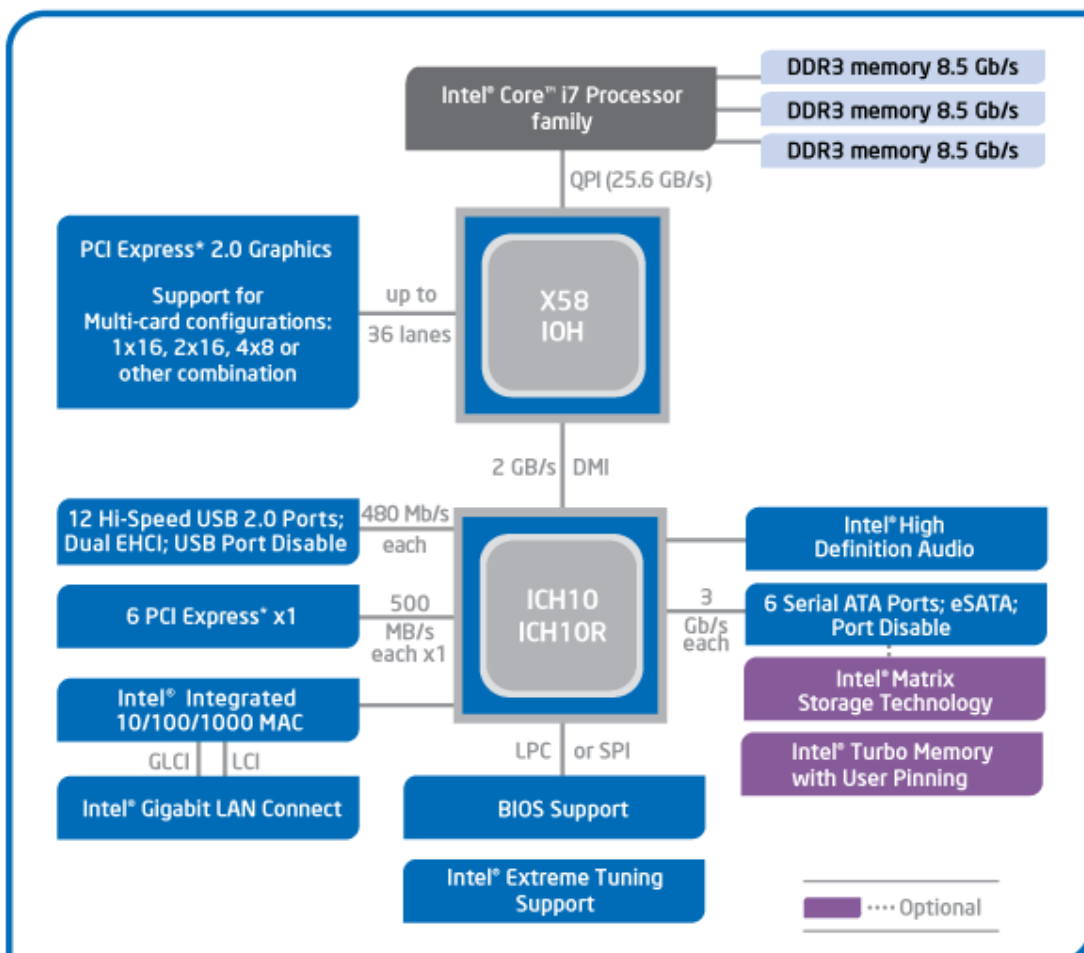
Intel Q965 Express Chipset Block Diagram



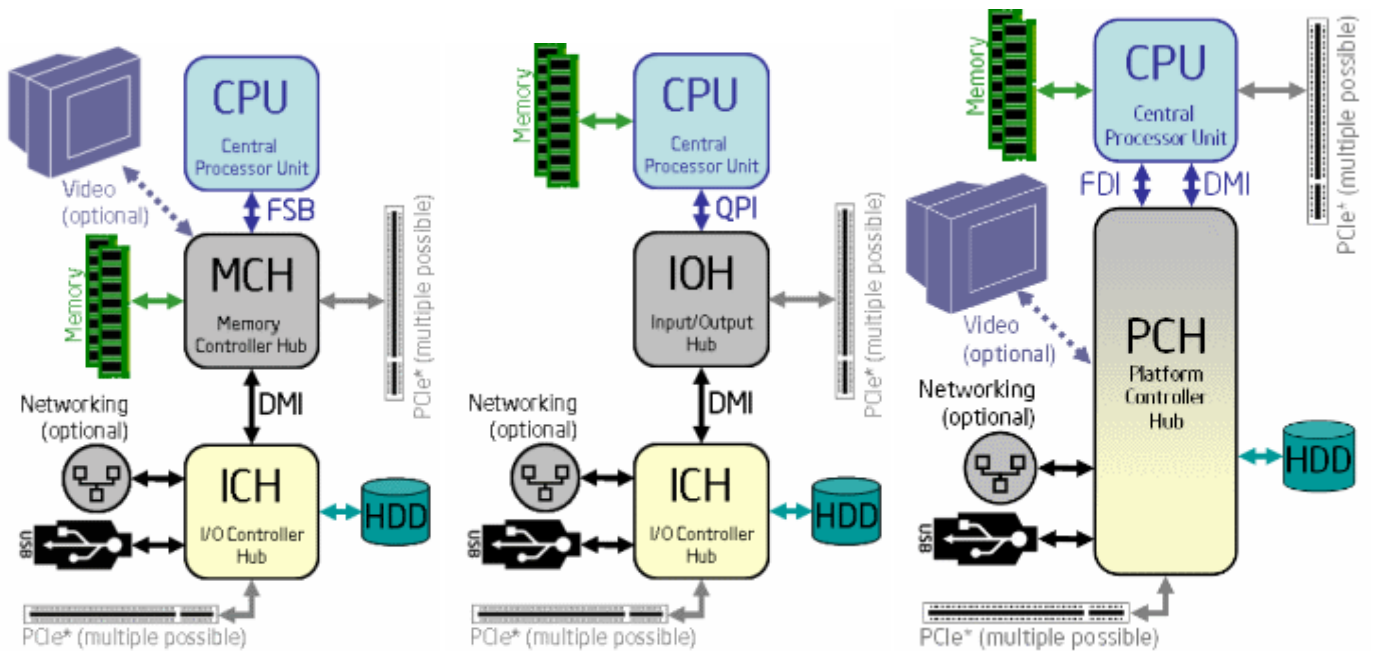
Intel® Q45 Express Chipset Block Diagram



Intel® X48 Express Chipset Block Diagram



Intel® X58 Express Chipset Block Diagram

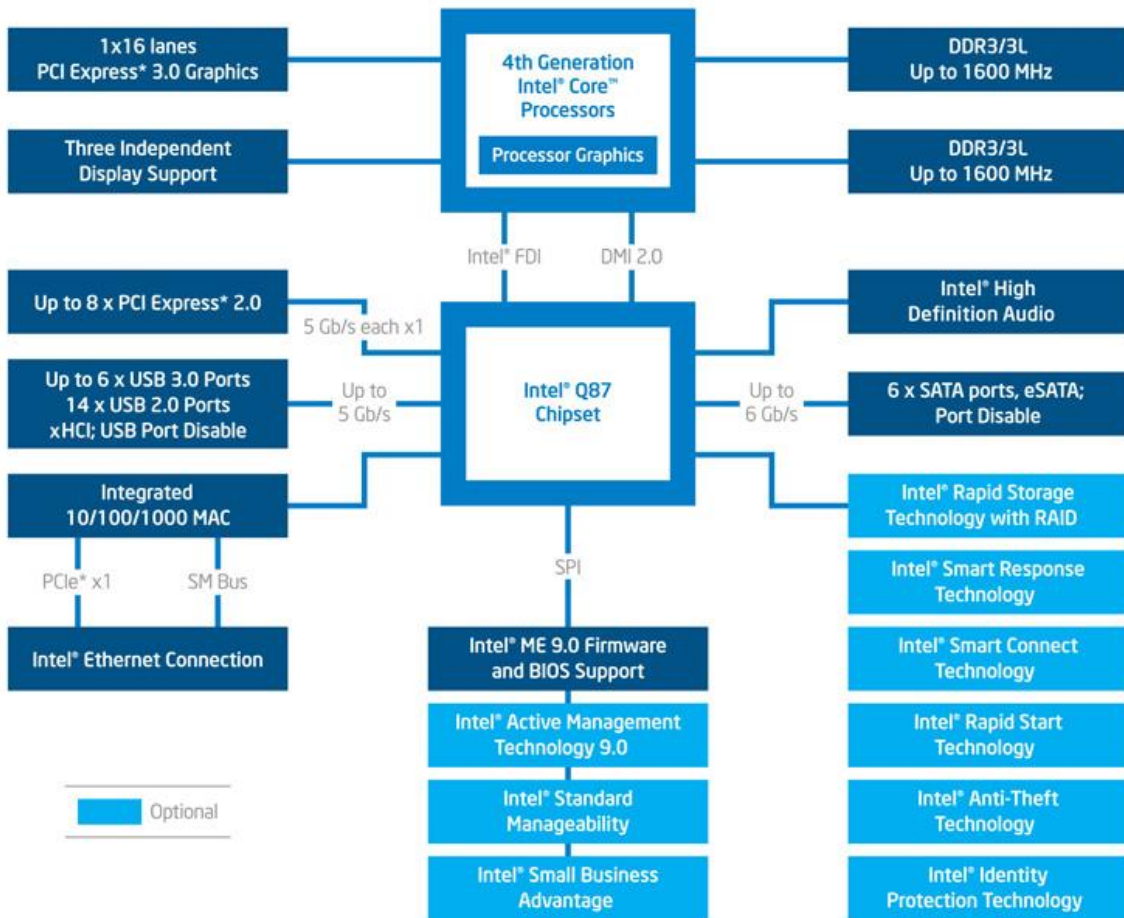
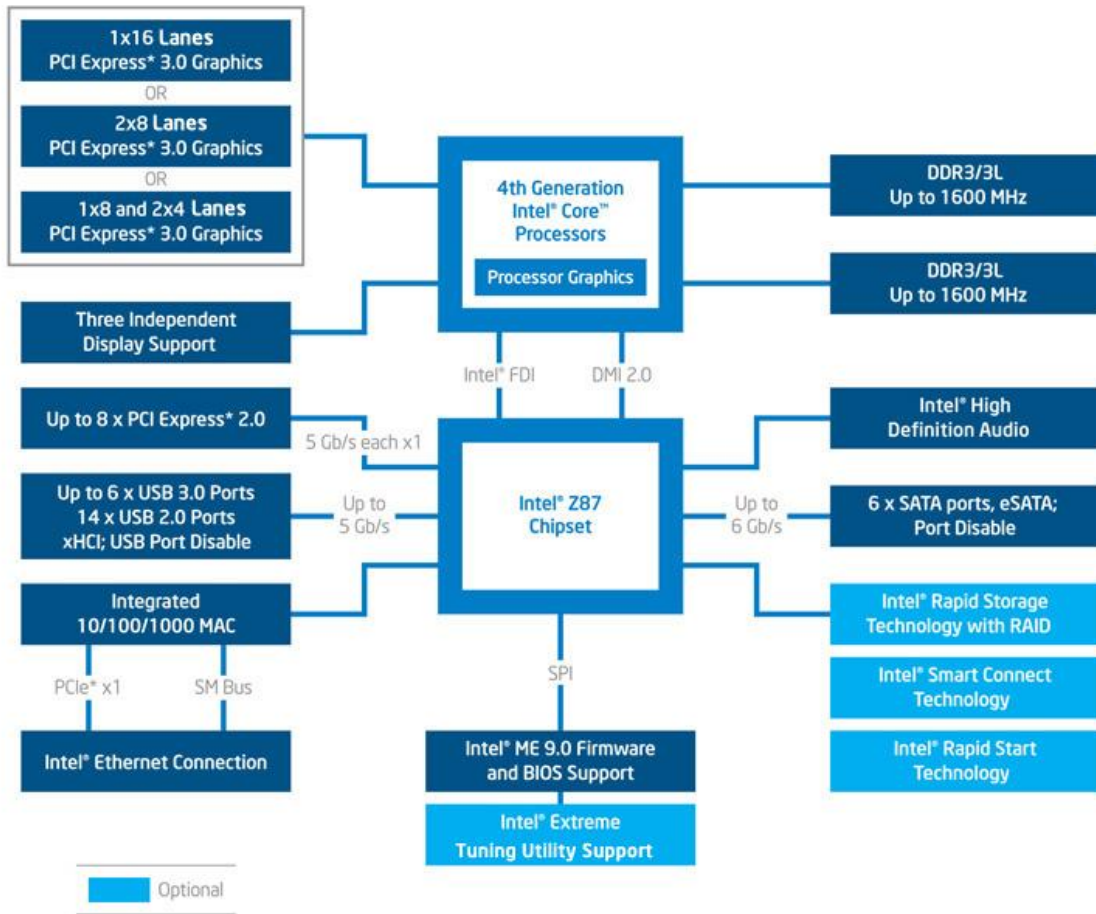


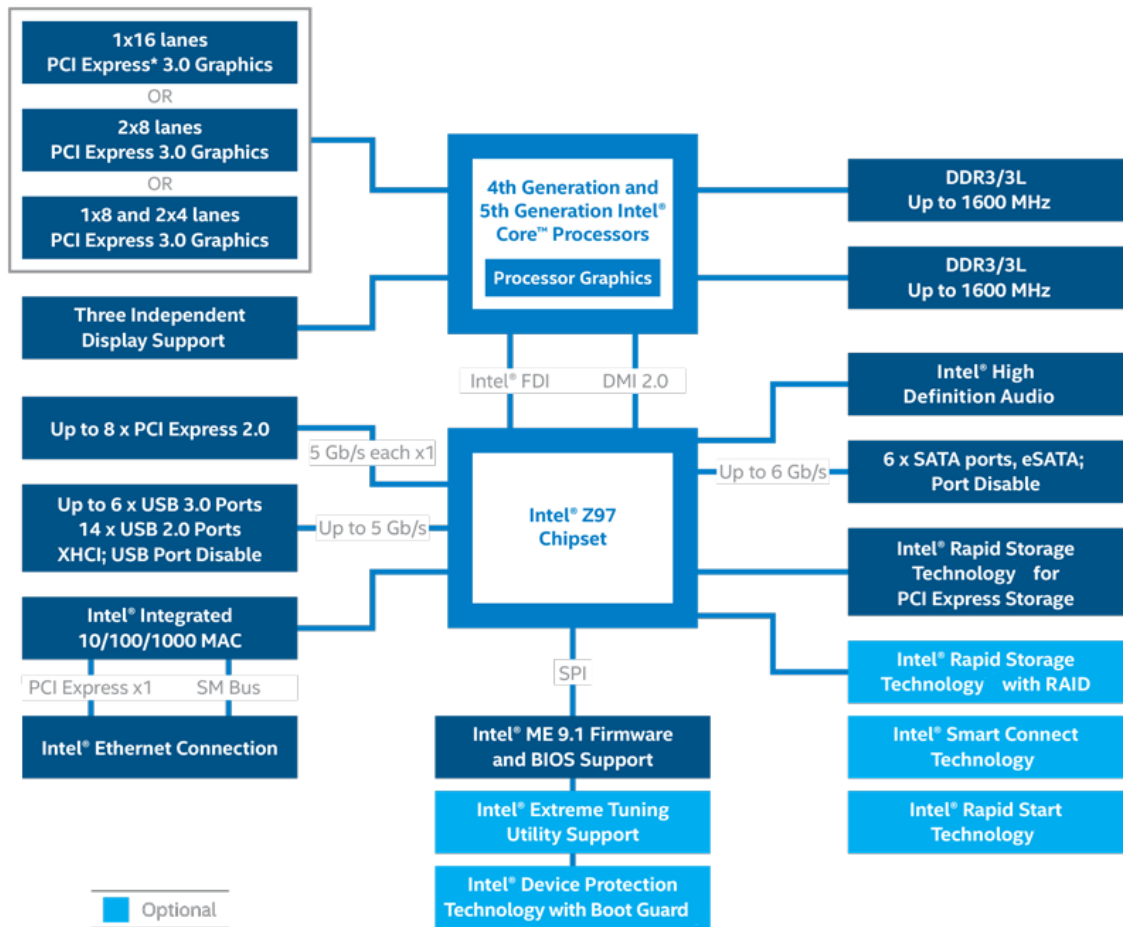
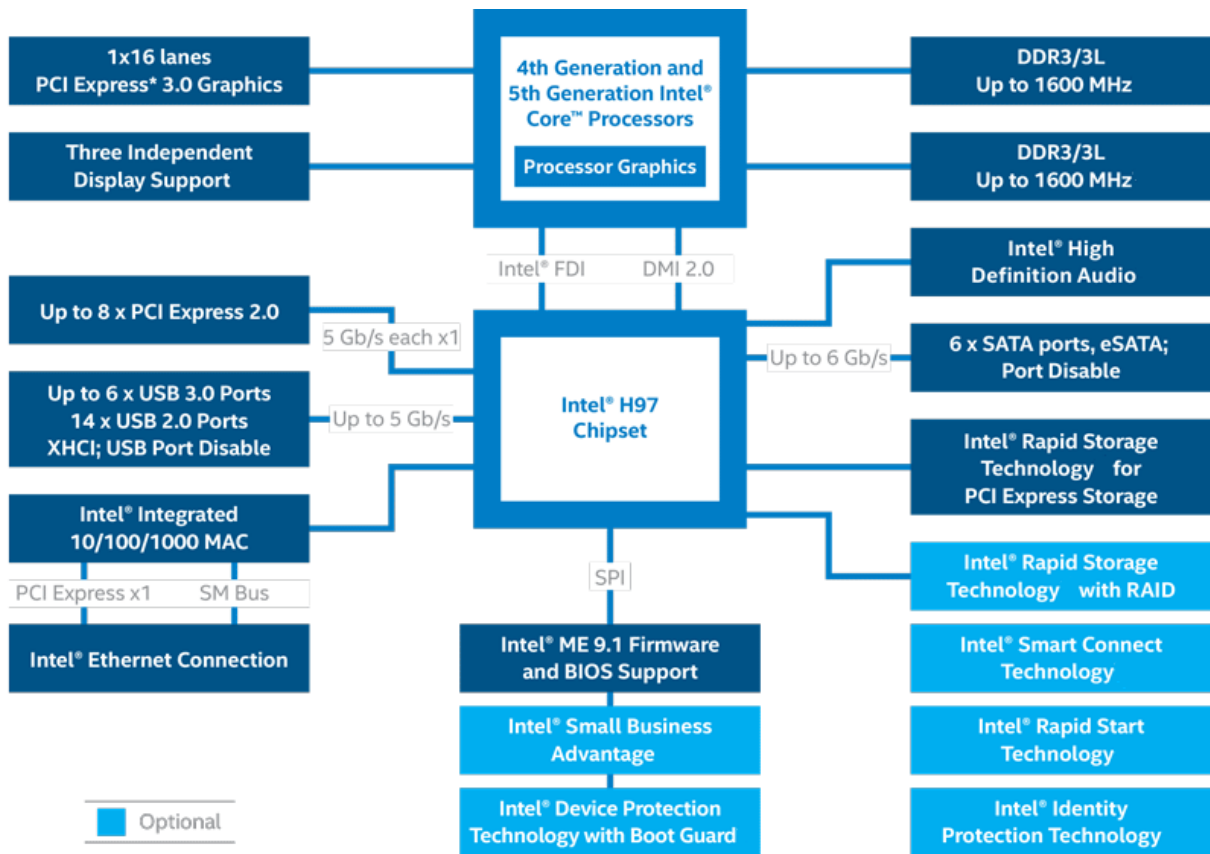
Čípsety a CPU 2008-2015

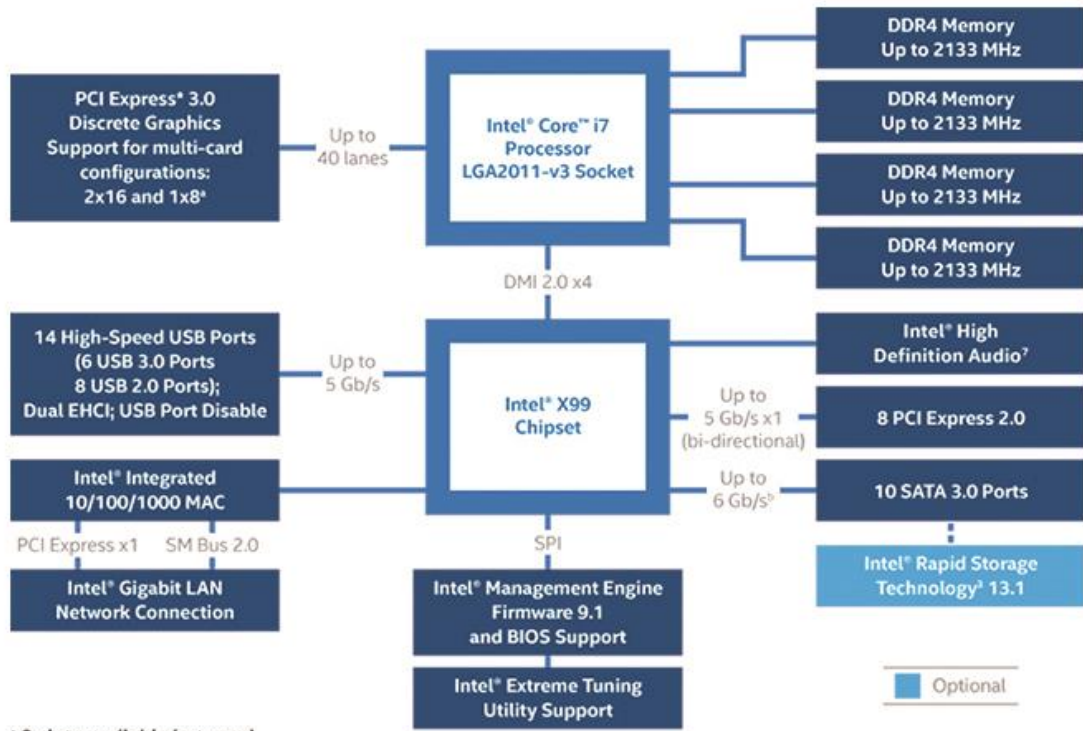
Rok	do 2008	2009-2010	2011	2012	2013	2014	2015
Chipset	do X48	X58/5x	6x	7x	8x	9x	9x/1xx
LGA	775	1366/1156	1155/2011	1155/2011	1150/2011	1150/2011	1150/1151/2011
CPU	Pentium Core 2	Core i7,i5,i3 1.gen.	Core i7,i5,i3 2.gen.	Core i7,i5,i3 3.gen.	Core i7,i5,i3 4.gen.	Core i7,i5,i3 4.gen./5.gen.	Core i7,i5,i3 5.gen./6.gen.
Litografia	45nm	45nm/32nm	32nm	22nm	22nm	22nm/14nm	14nm
Codename	Conroe	Penryn/ Nehalem	Westmere/ Sandy Bridge	Ive Bridge	Haswell	Haswell/ Broadwell	Broadwell/ Skylake

IDE-R Function

The IDE-R function is an IDE Redirection interface that provides client connection to management console ATA/ATAPI devices such as hard disk drives and optical disk drives. A remote machine can setup a diagnostic SW or OS installation image and direct the client to boot an IDE-R session. The IDE-R interface is the same as the IDE interface although the device is not physically connected to the system and supports the ATA/ATAPI-6 specification. IDE-R does not conflict with any other type of boot and can instead be implemented as a boot device option. The Intel AMT solution will use IDE-R when remote boot is required. The device attached through IDE-R is only visible to software during a management boot session. During normal boot session, the IDE-R controller does not appear as a PCI present device.



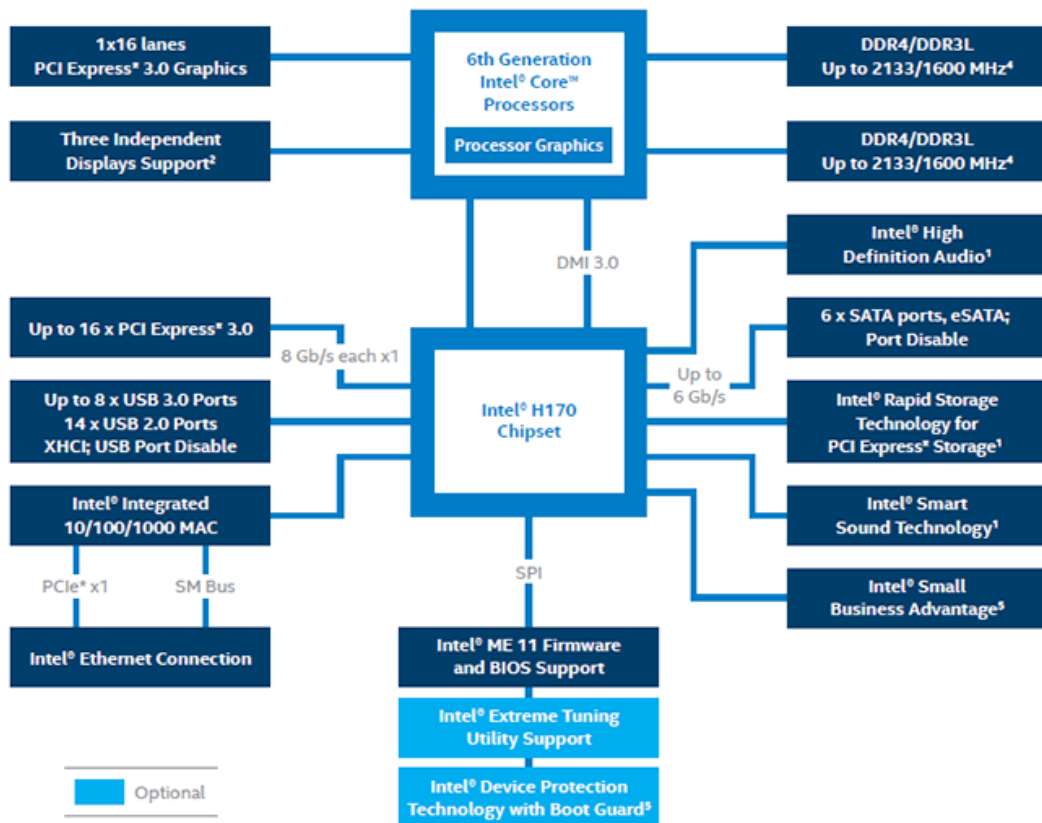




^a 3 slots available but need additional logic onboard to support more slots. 5x8 configuration requires additional system clocks to be provided by third party components.

^b All SATA ports capable of 6 Gb/s.

Intel® H170 Chipset Block Diagram



Intel® Q170 and Q150 Chipsets Block Diagram

